

1/22

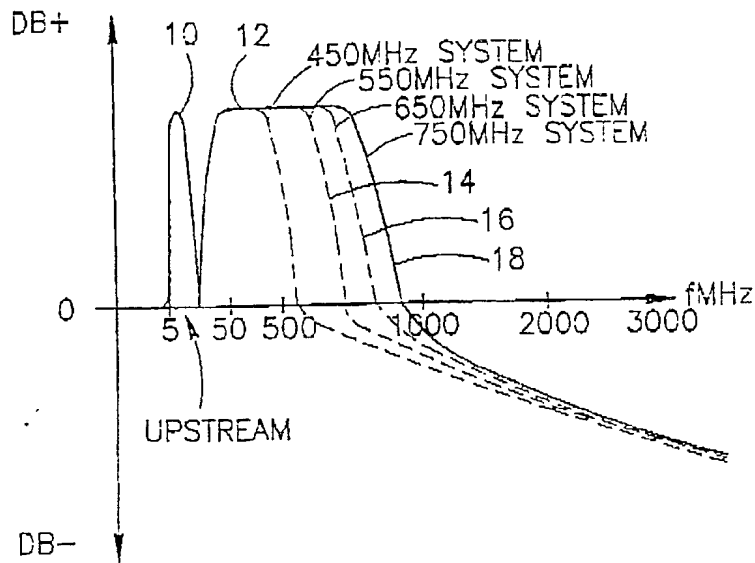


FIG. 1A

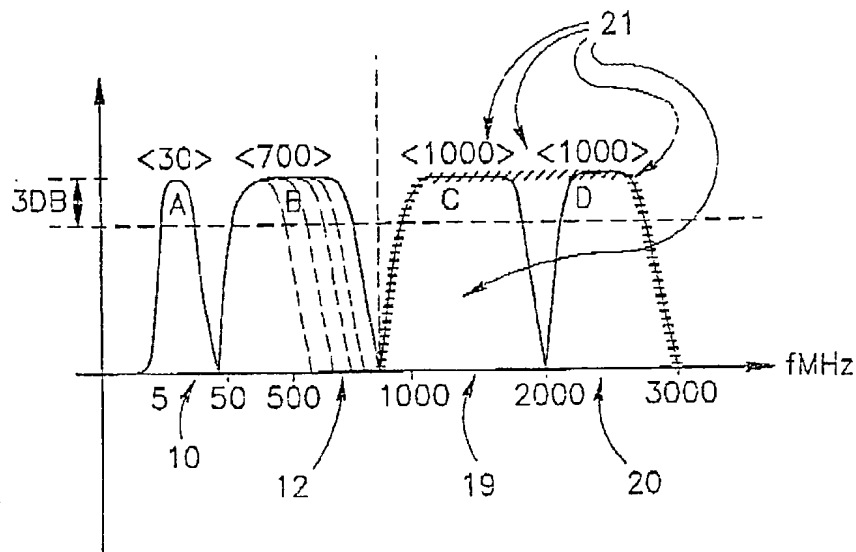
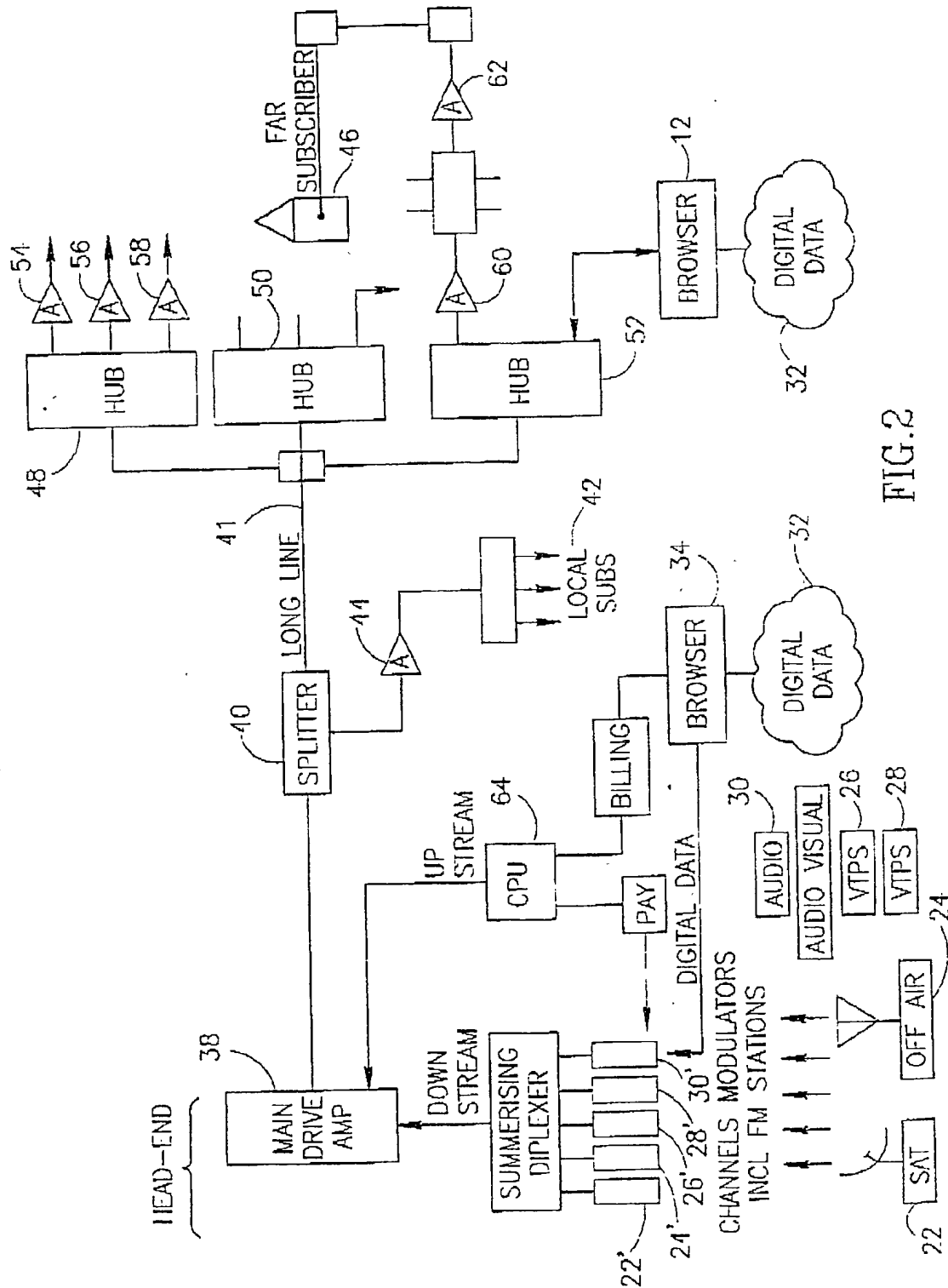


FIG. 1B

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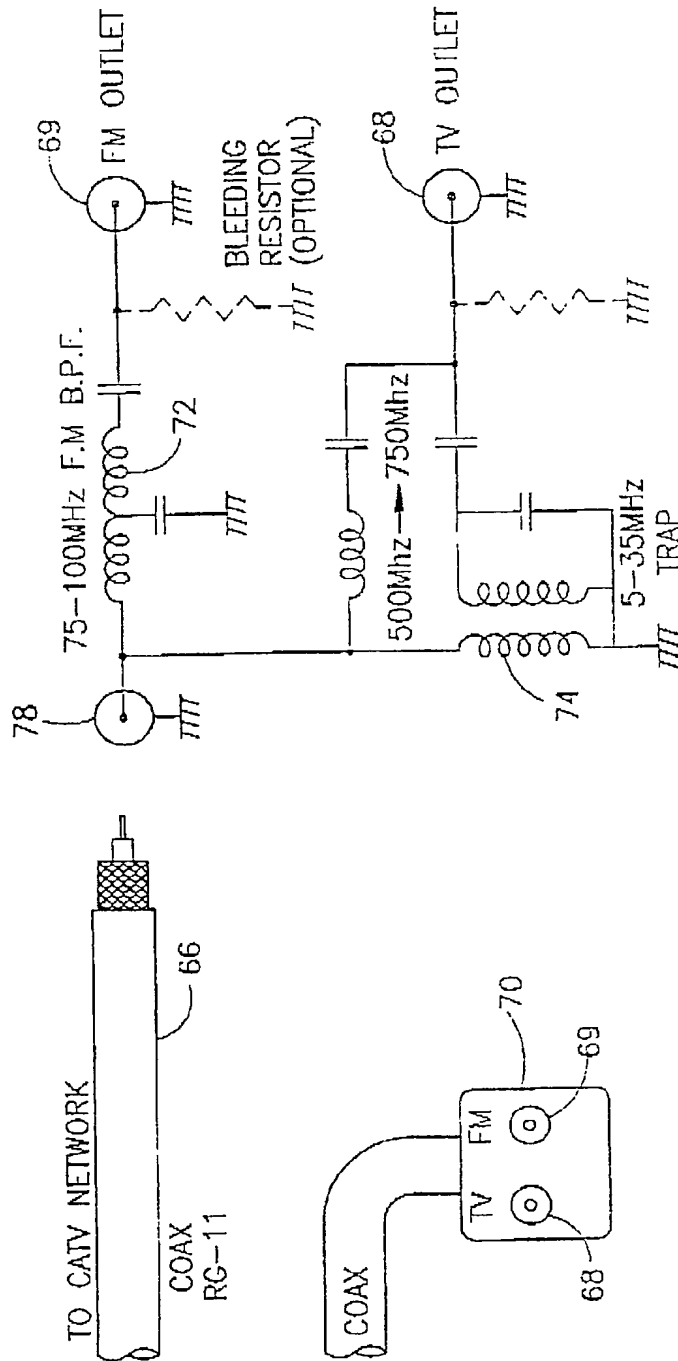


FIG. 3

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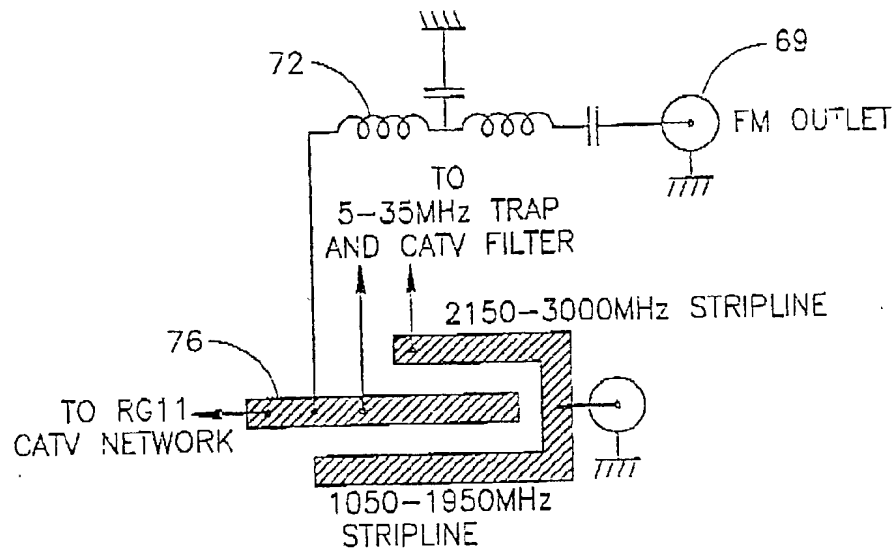


FIG.4

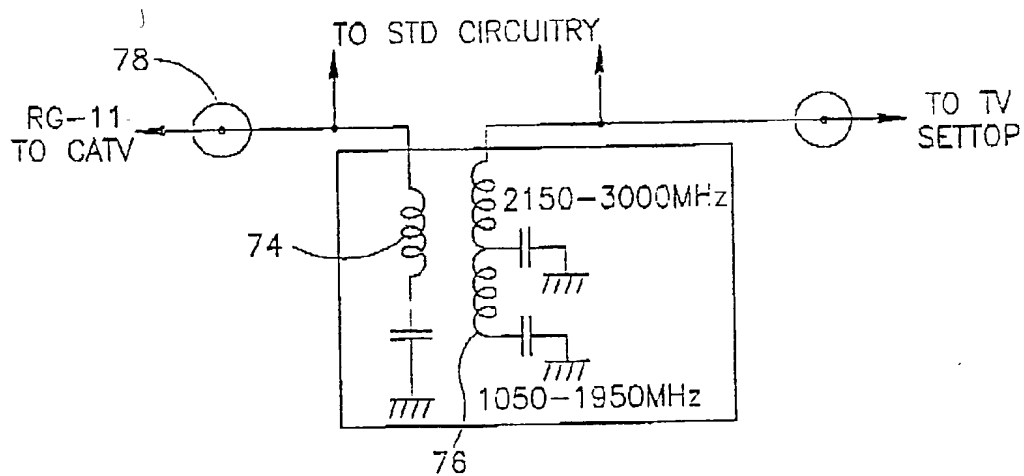


FIG.5

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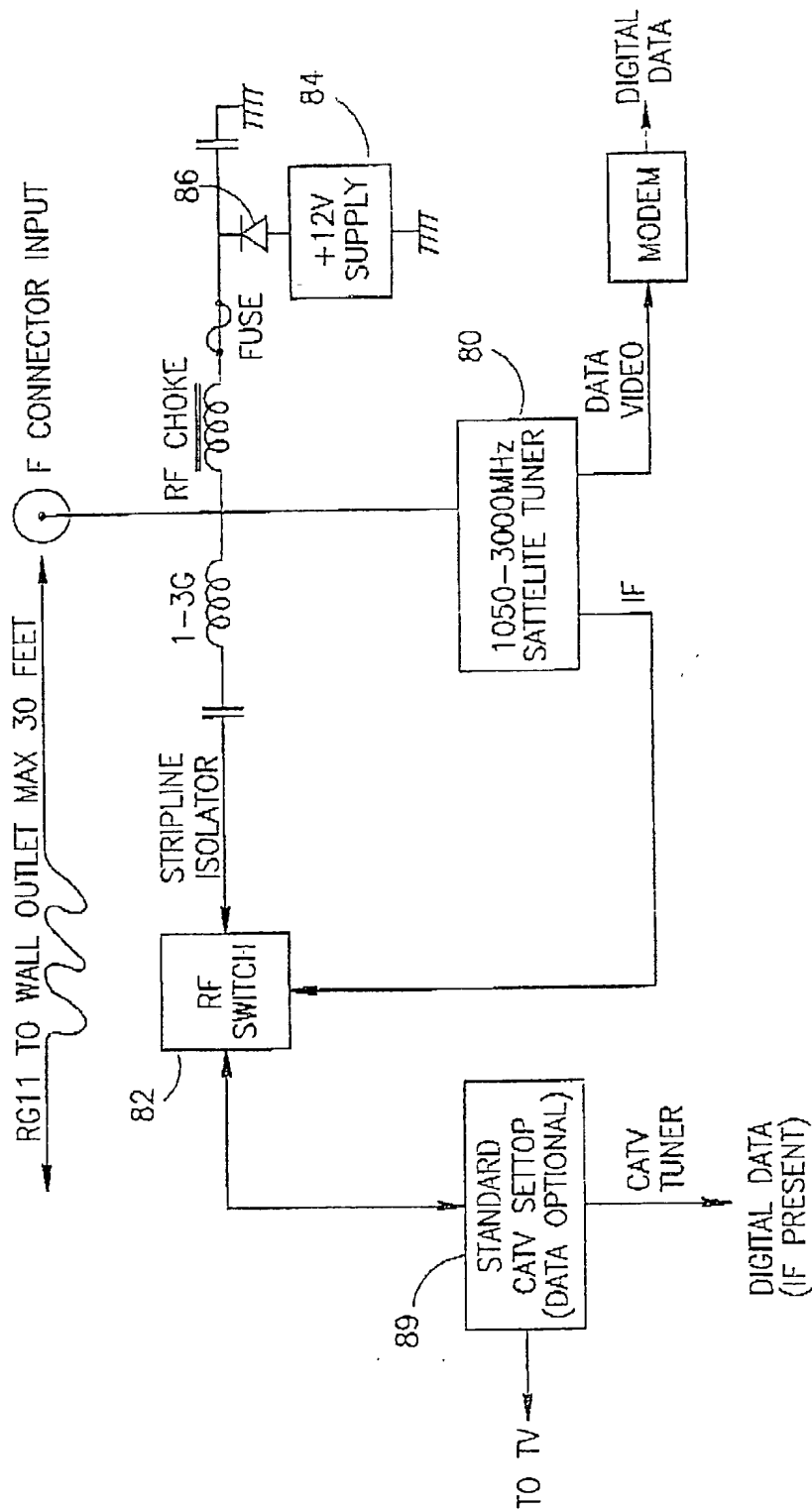


FIG.6

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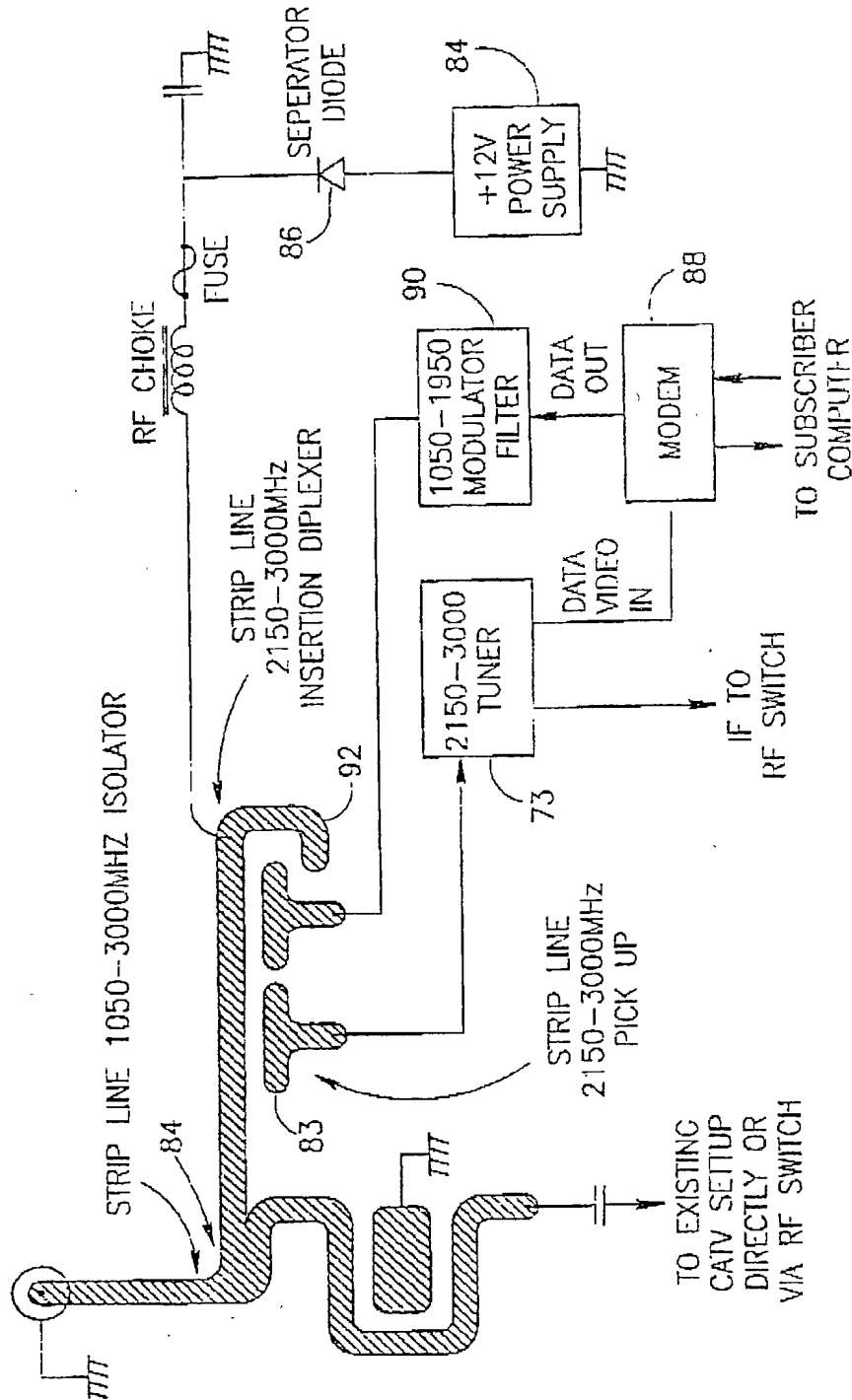


FIG. 7

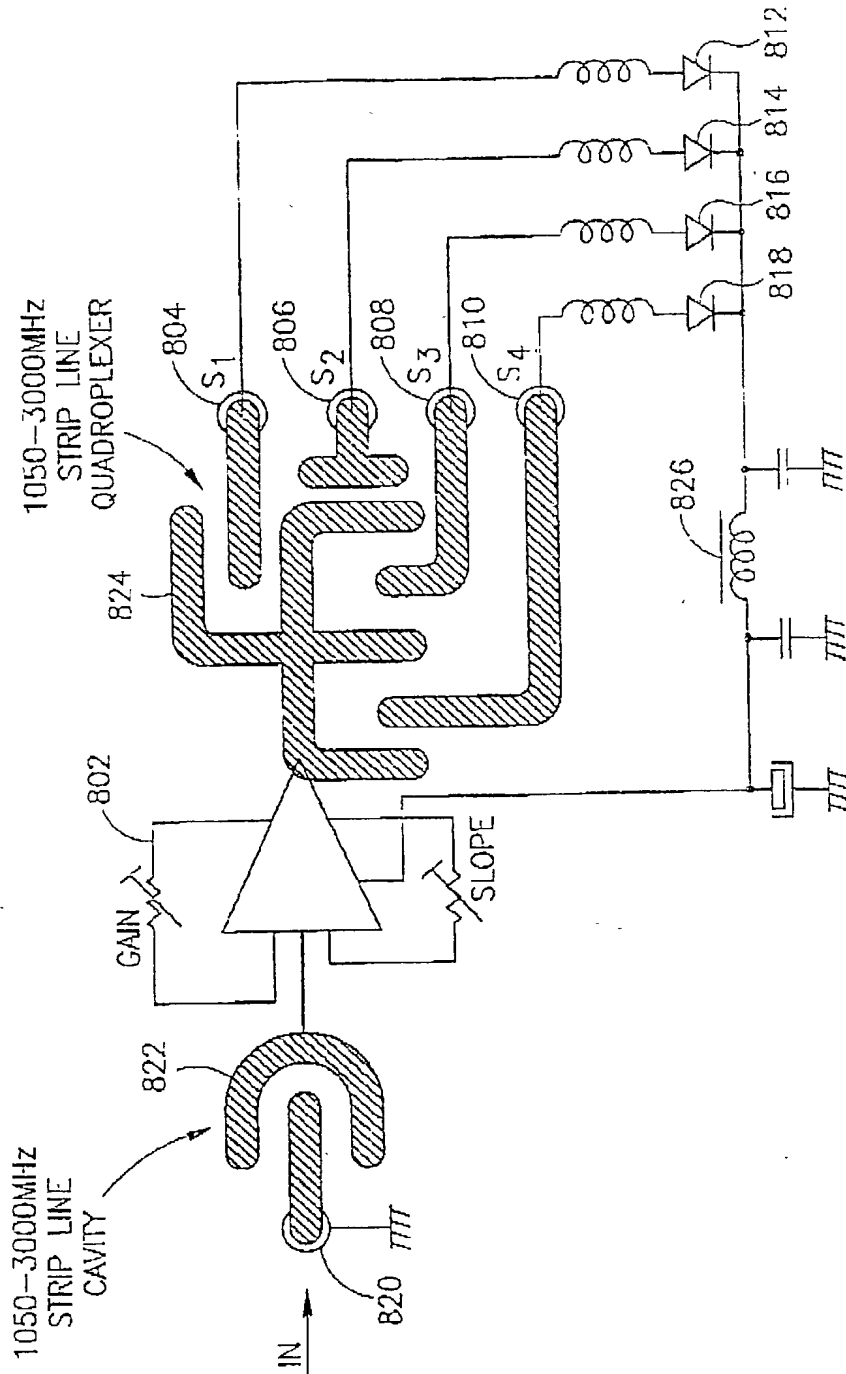


FIG.8

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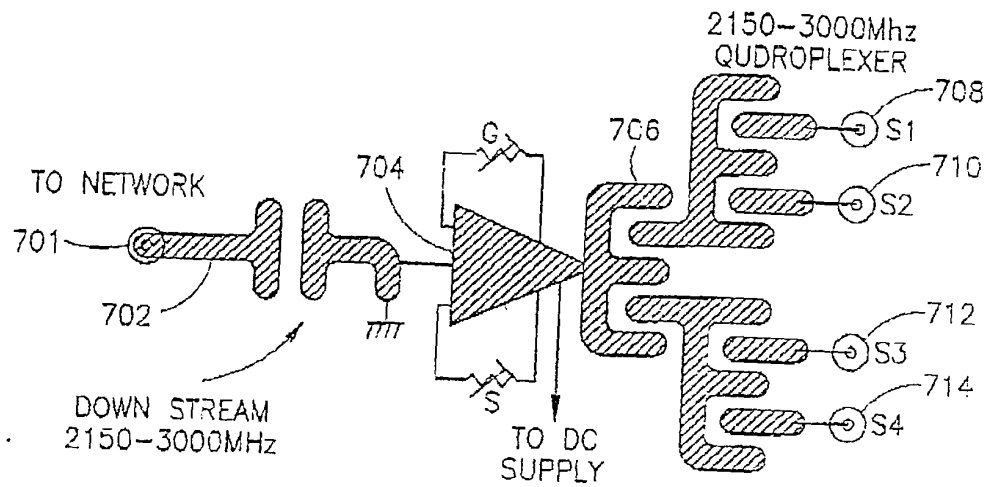


FIG.9A

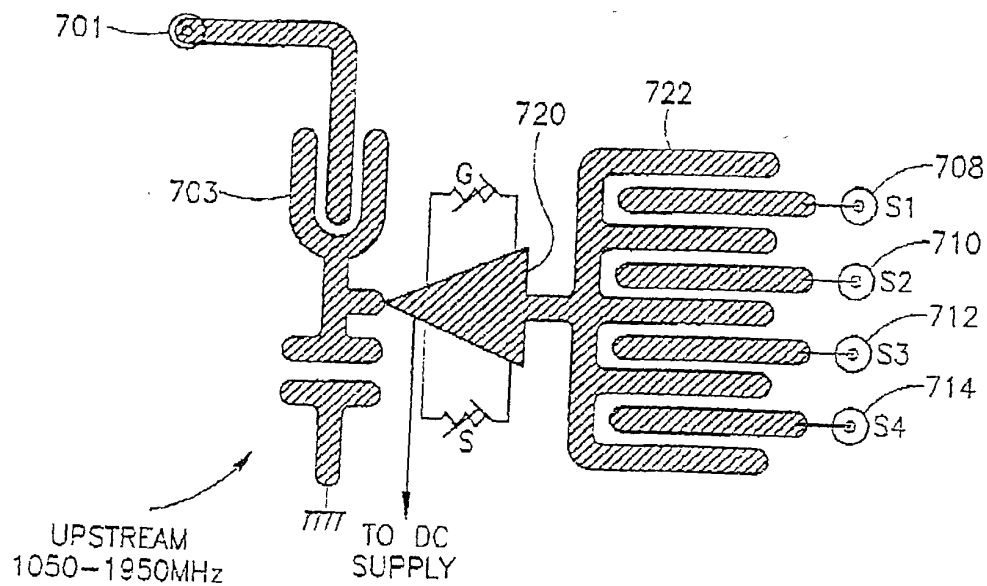


FIG.9B



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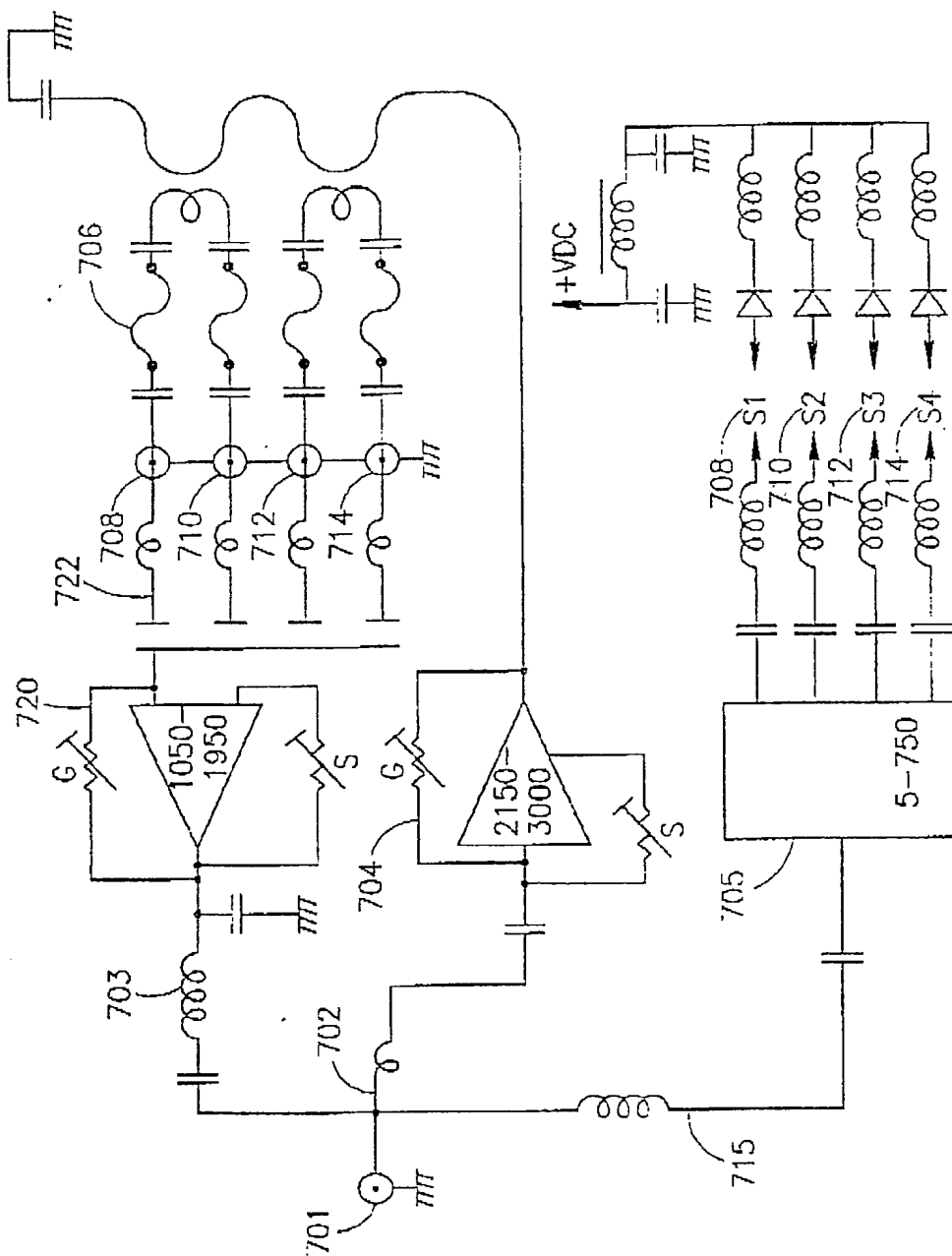
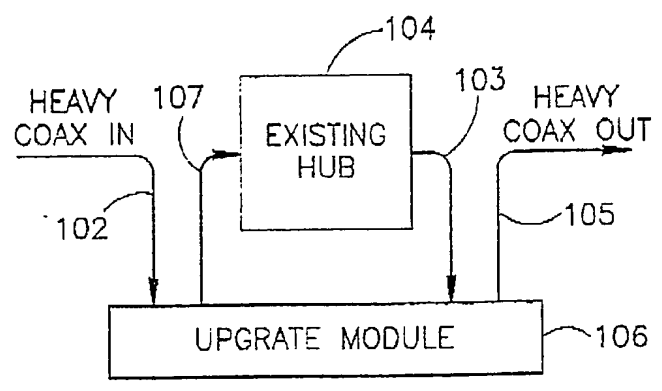
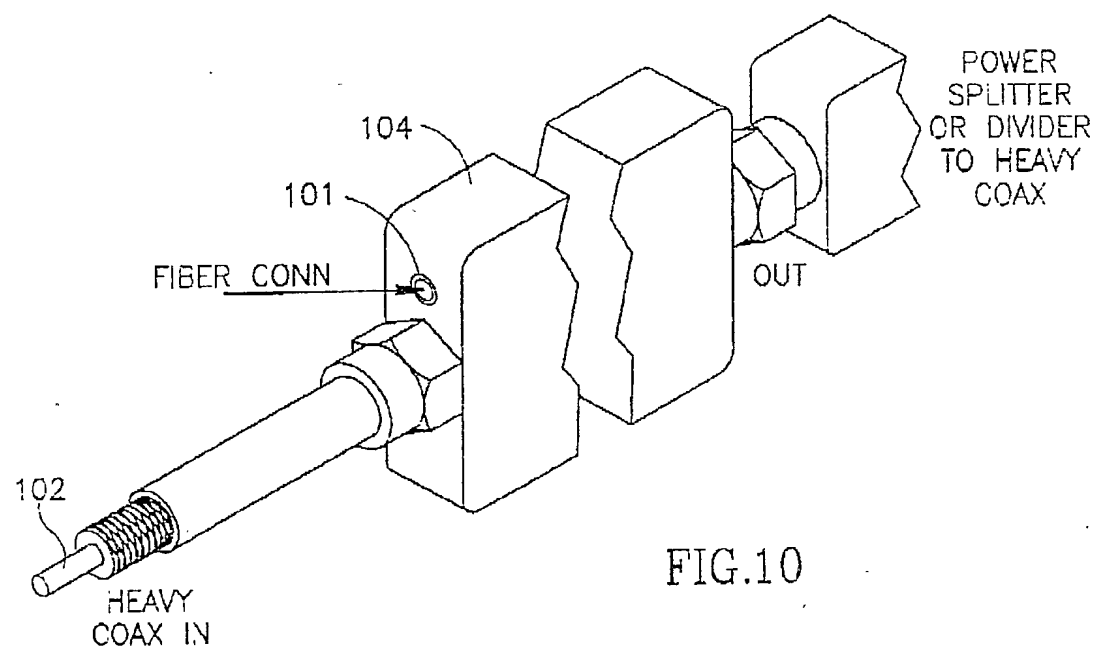


FIG. 9C

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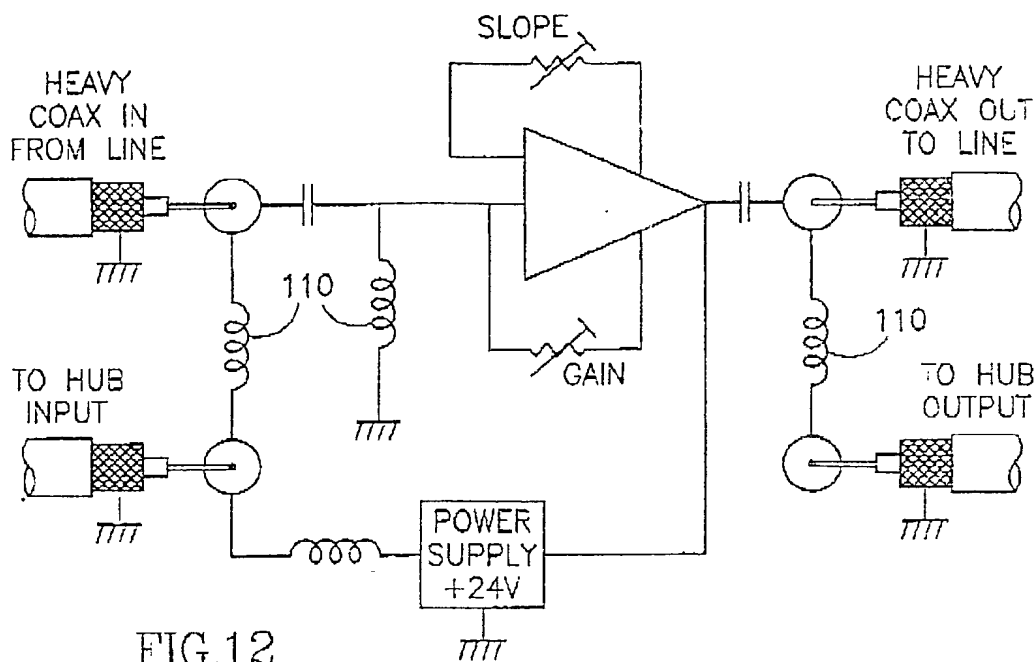


FIG.12

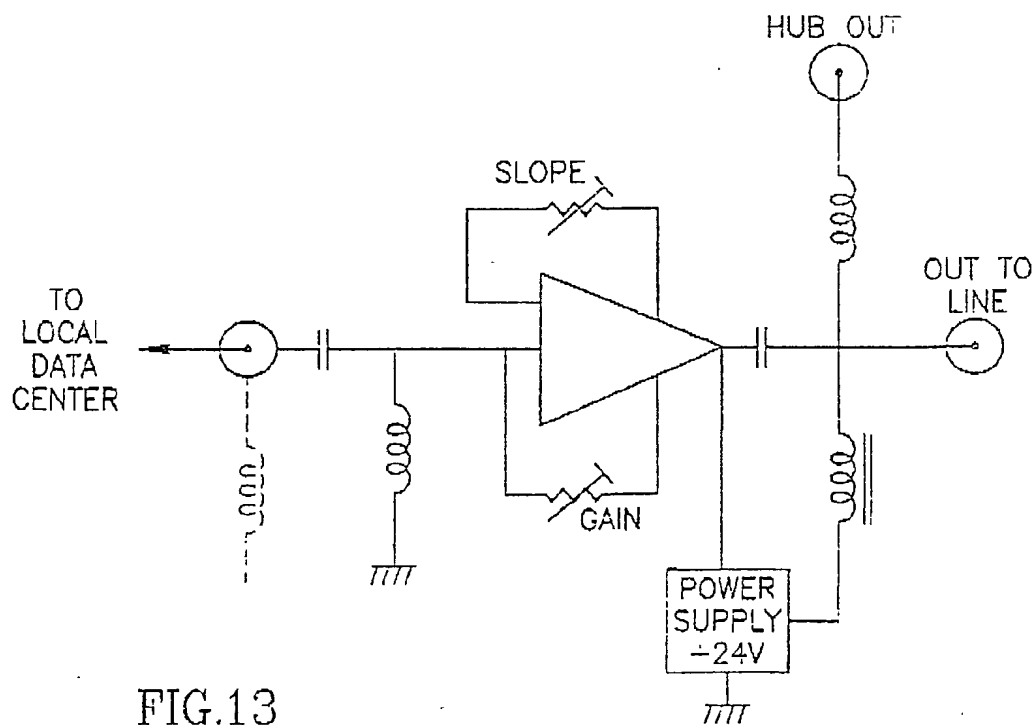


FIG.13

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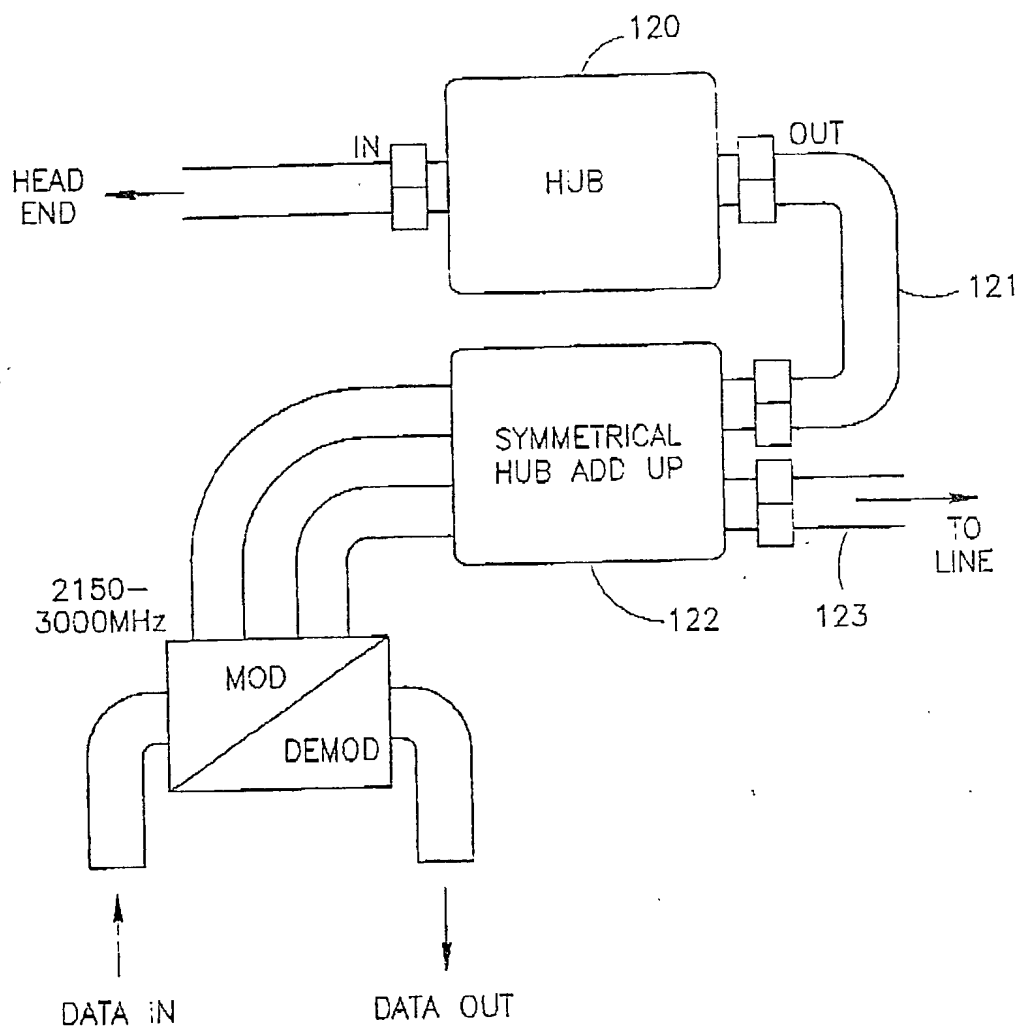


FIG.14

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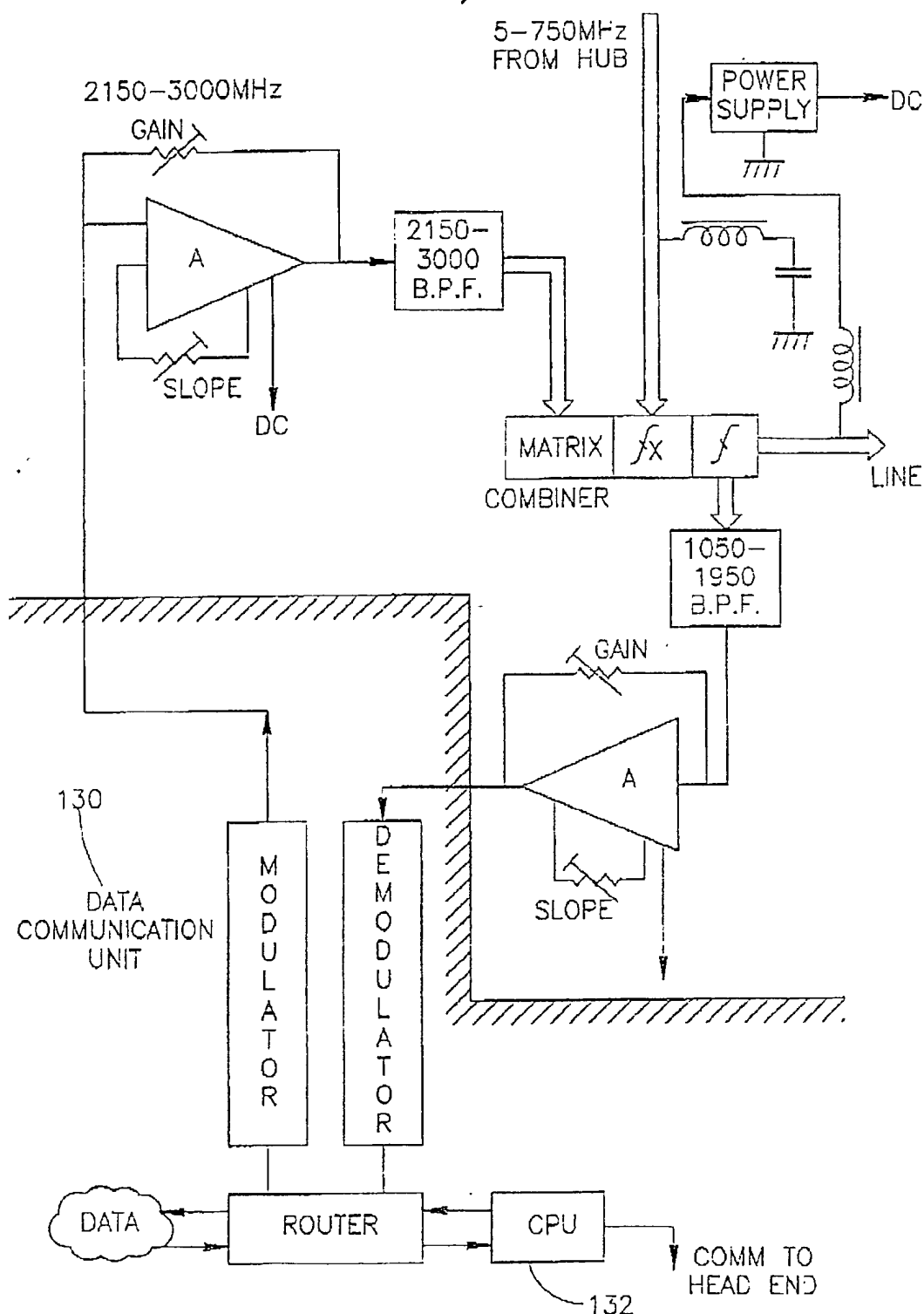


FIG.15

The diagram illustrates a radio receiver system architecture. It features a central variable frequency converter (VFC) block, labeled '140', which contains an internal amplifier 'A' and is tuned to the '1050-1950' range. The system has two main signal paths: an input path and an output path. The input path starts with a 'LINE IN' terminal, followed by a '2150-3000 B.P.F.' (Band Pass Filter) block, then the VFC block, and finally a '5-750 B.P.F.' block before reaching the 'TO EXISTING DEVICE IN' terminal. The output path starts from the 'TO EXISTING DEVICE OUT' terminal, passes through a '5-750 B.P.F.' block, then the VFC block, and a '2150-3000 B.P.F.' block before reaching the 'LINE OUT' terminal. A 'POWER SUPPLY' block provides a 'DC' voltage to the system, which is connected to the VFC block and the output path. The output path also includes an 'RF CHOKE HIGH POWER' component. The VFC block is shown with 'IN' and 'OUT' terminals and a 'DC' input. The output path is also connected to a 'TO EXISTING DEVICE OUT' terminal. The diagram includes various electronic symbols such as ground connections (three parallel lines), inductors (coiled lines), and capacitors (two parallel lines with a diagonal line through them).

FIG.16

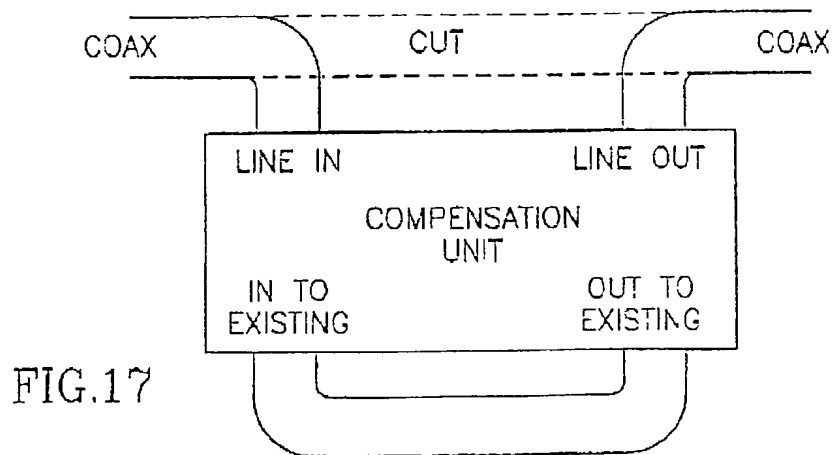


FIG.17

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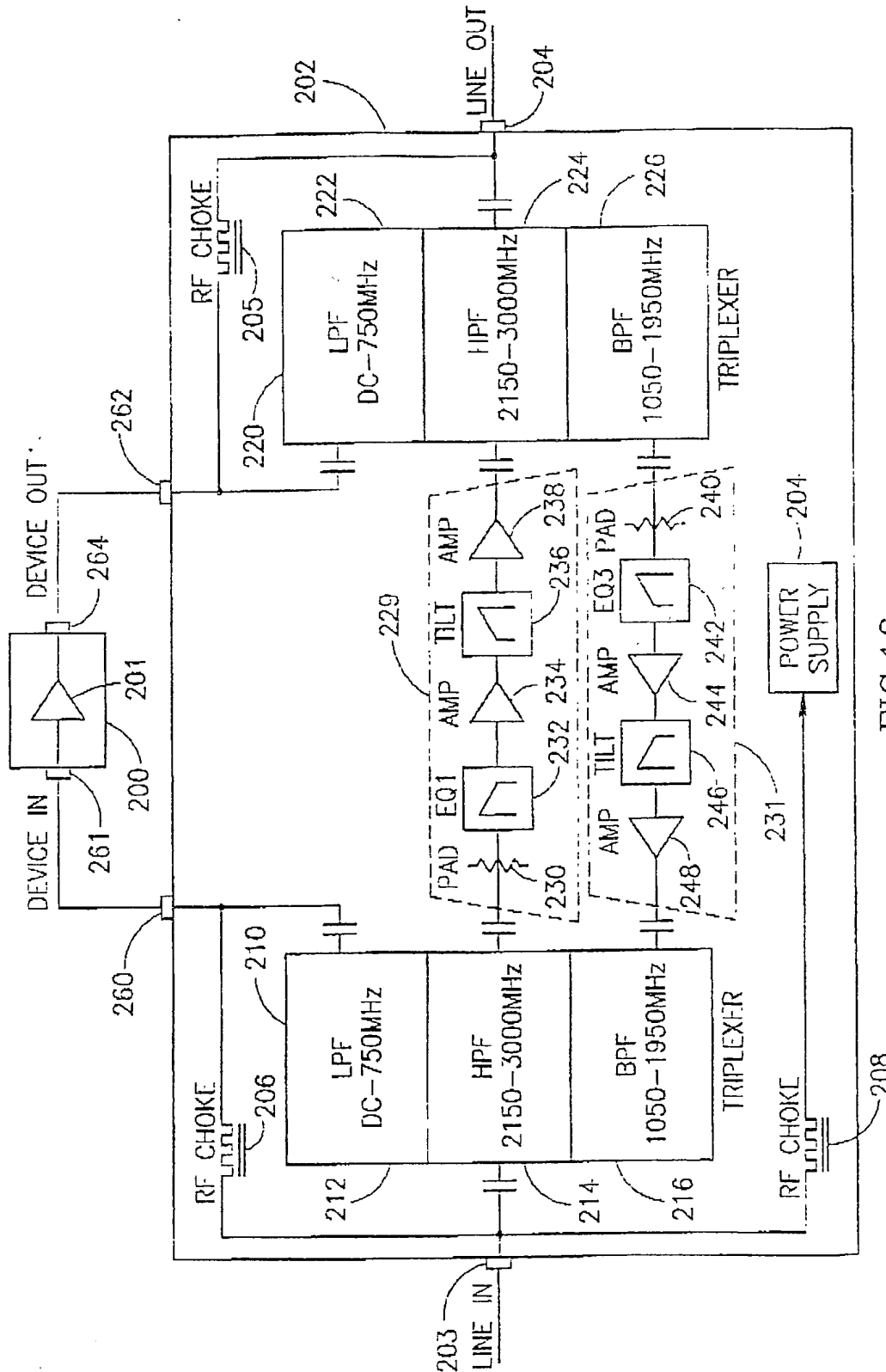


FIG.18

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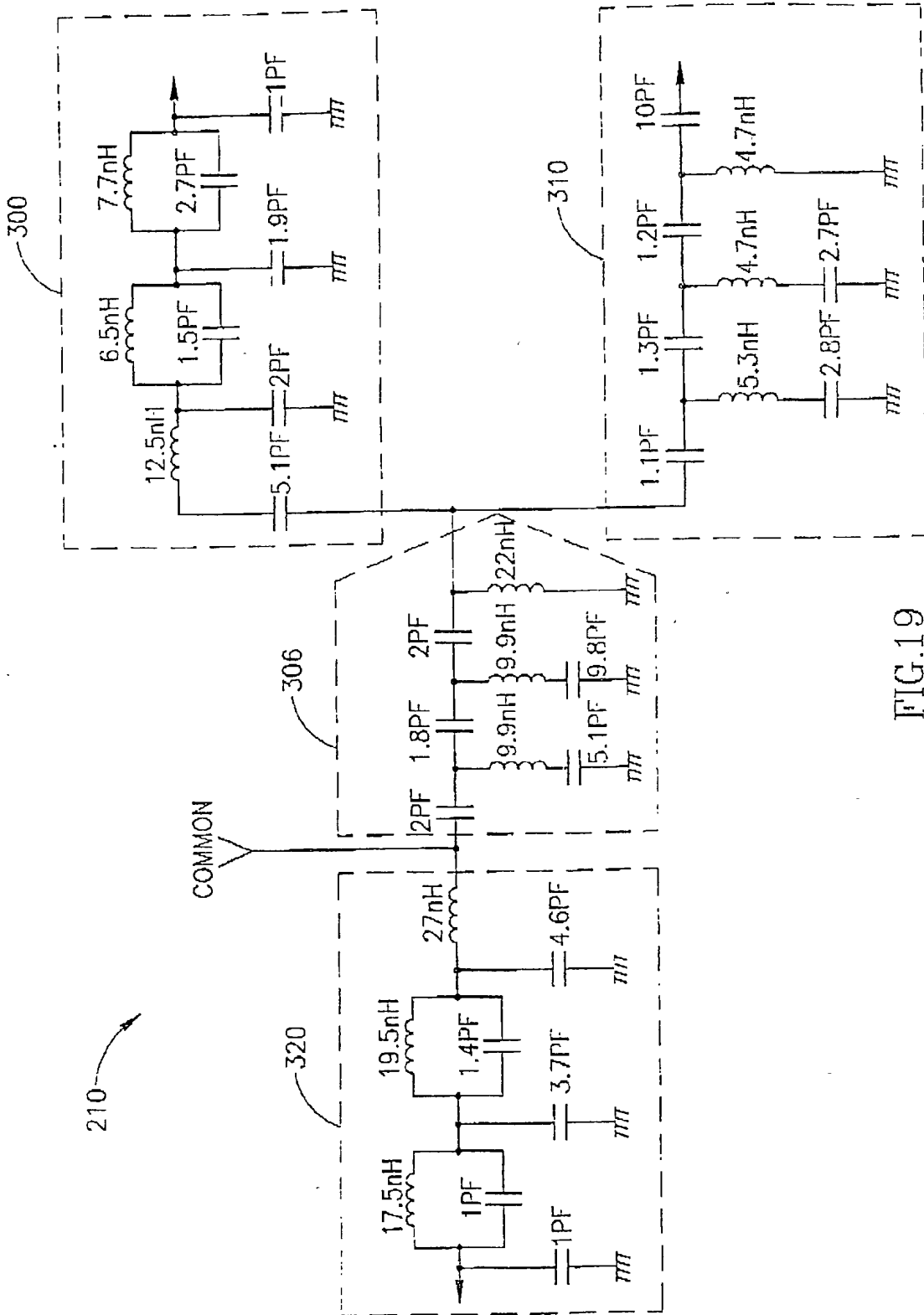


FIG.19





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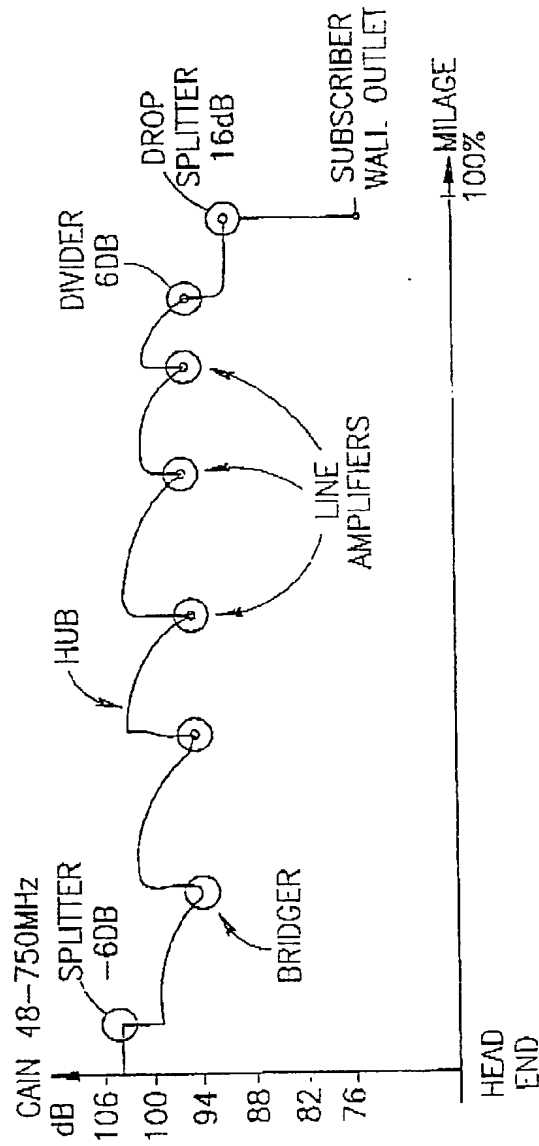


FIG.21

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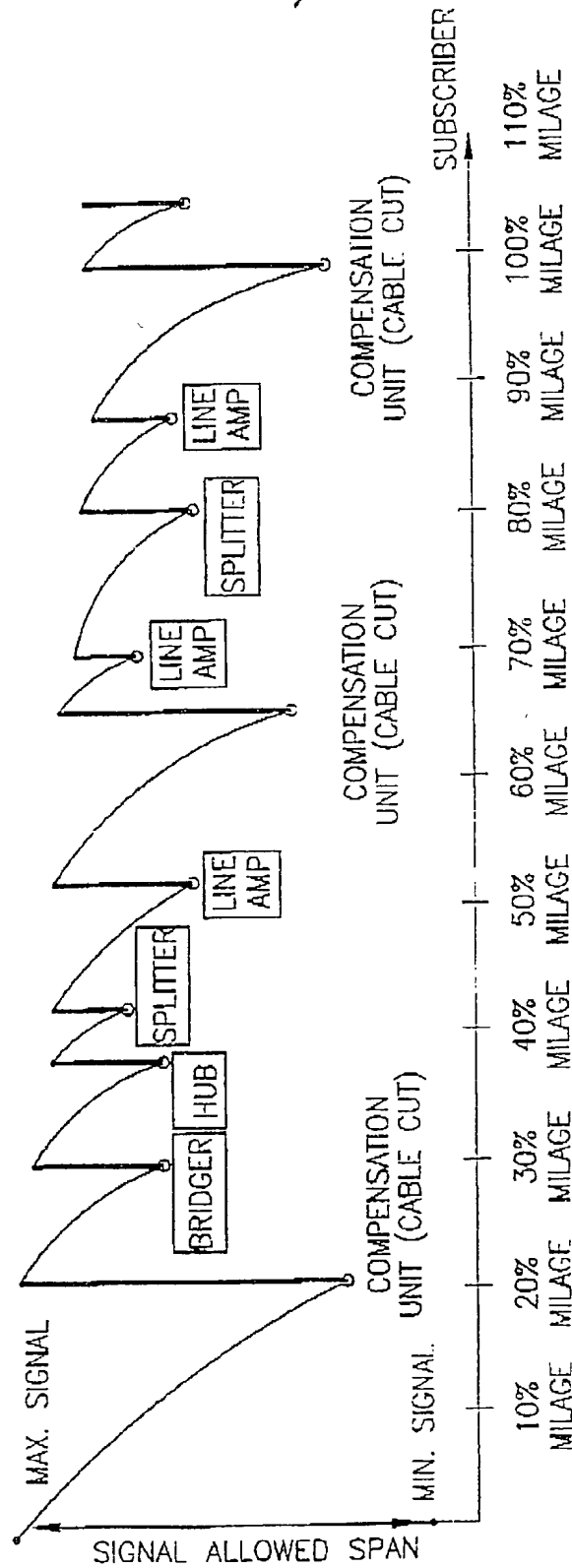


FIG.22

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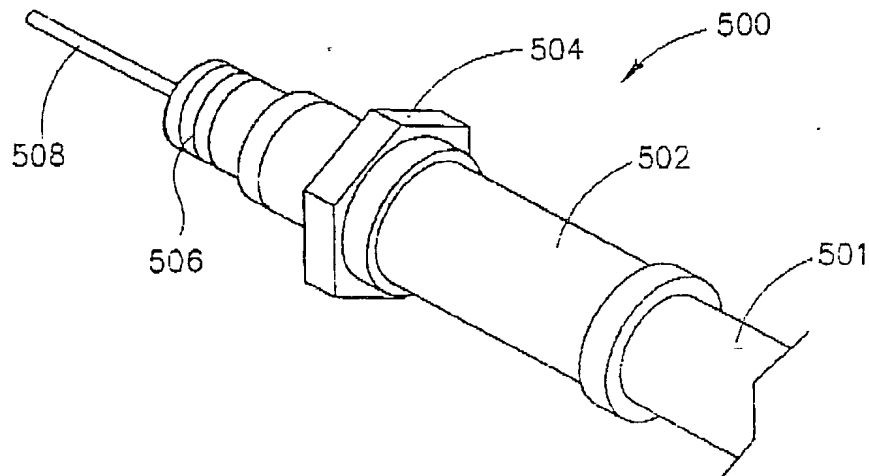


FIG. 23

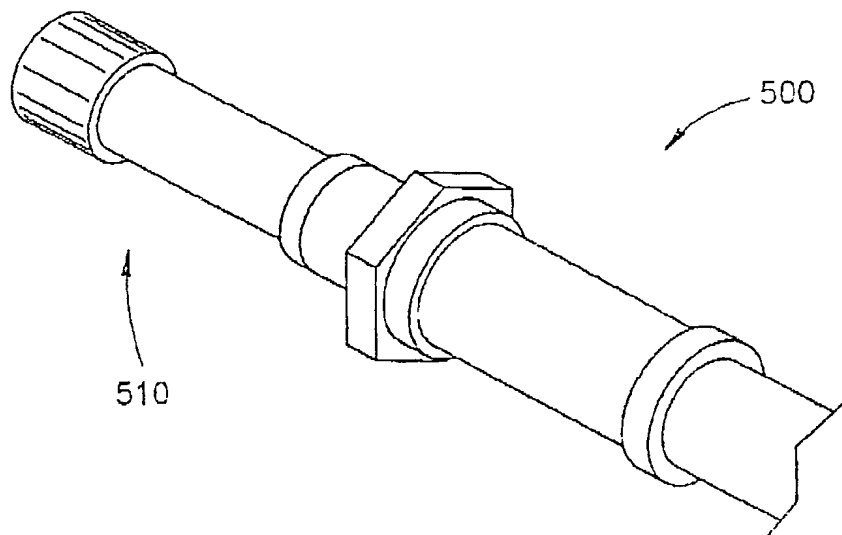


FIG. 24

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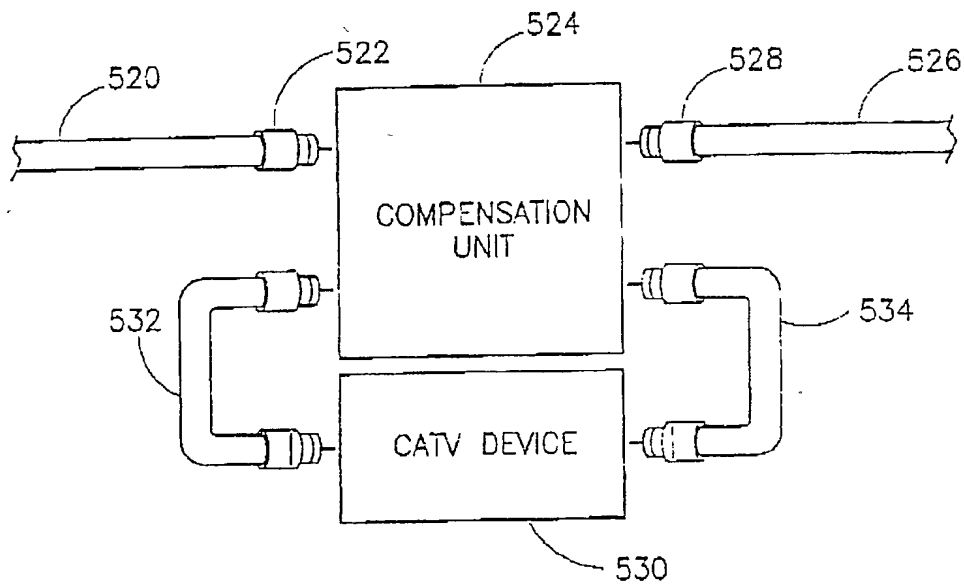


FIG.25

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